



CMTFETBIAS2

Single FET Bias Supply

REGULATED DC OUTPUTS

- Gate 1: -5.0 V to +5.0 V Max Current = 2 mA (Full scale current display)
- Gate 2: -5.0 V to +5.0 V Max Current = 2 mA (Full scale current display)
- Drain: -0 V to +5.0 V Max Current = 200 mA (Full scale current display)

POWER INPUT

- 120VAC

SIZE

- 17.5" (w) x 11.8" (d) x 3.5" (h)



Description

The CMTFETBIAS2 is a low noise, laboratory grade, adjustable power supply, designed specifically to bias HEMT and FET low noise MMICs. The design of the bias supply is based on the Linear Technology LM399 precision reference. This device is a 6.95 V shunt reference diode integrated with a heater for temperature stability. The temperature coefficient of this device is 0.3 ppm/C. The typical RSM noise of the device is 7 uV in the bandwidth of 10 Hz to 10 KHz. 4½ digit panel meters display the gate and drain voltage and current.

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